

MAR 15 2006

## TO THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. .... 10/655,699  
Filing Date ..... September 5, 2003  
Inventor ..... Garo J. Derderian et al.  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2818  
Examiner ..... Renee R. Berry  
Attorney Docket No. .... MI22-2307  
Customer No. .... 021567  
Title: Methods of Depositing a Silicon Dioxide Comprising Layer in the  
Fabrication of Integrated Circuitry, and a Method of Forming Trench  
Isolation in the Fabrication of Integrated Circuitry

TO: Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

STATUS INQUIRY

1. More than six and one-half (6½) months have passed since the filing of a response to the Patent Office's August 24, 2005 Office Action in this patent application. No communication from any examiner of the Patent and Trademark Office has been received since that date.
2. Kindly advise the undersigned of the present status of this patent application, in particular, when the undersigned might reasonably expect to receive further action or allowance. A Status Inquiry Reply is provided as the second page hereof. Please complete and then fax said second page/reply to the facsimile number indicated thereon.

Dated: 3-13-06By: 

Telephone No.: (509) 624-4276  
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(Status Inquiry—page 1 of 2)

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**STATUS INQUIRY REPLY**

**RE: PATENT APPLICATION SERIAL NO. 10/655,699:**

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